

FIG. 1

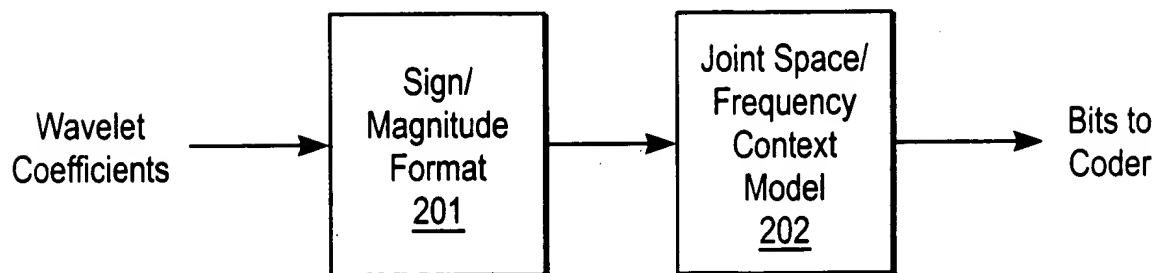


FIG. 2

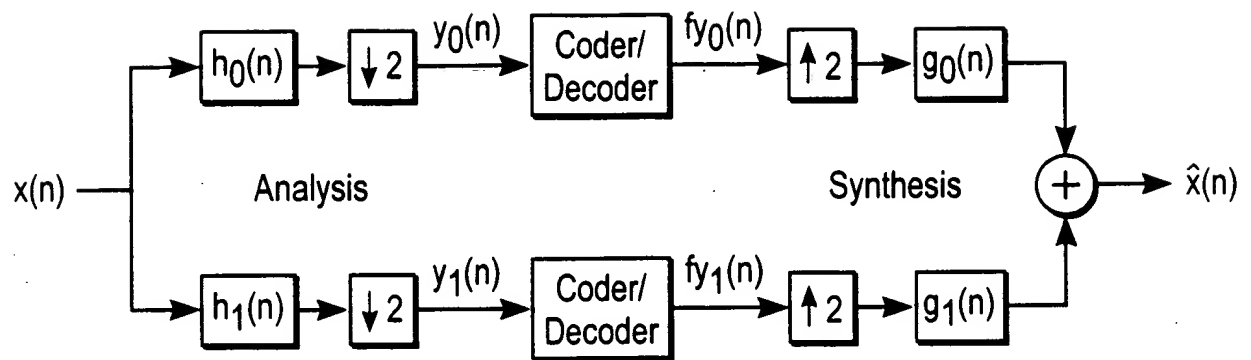


FIG. 3A

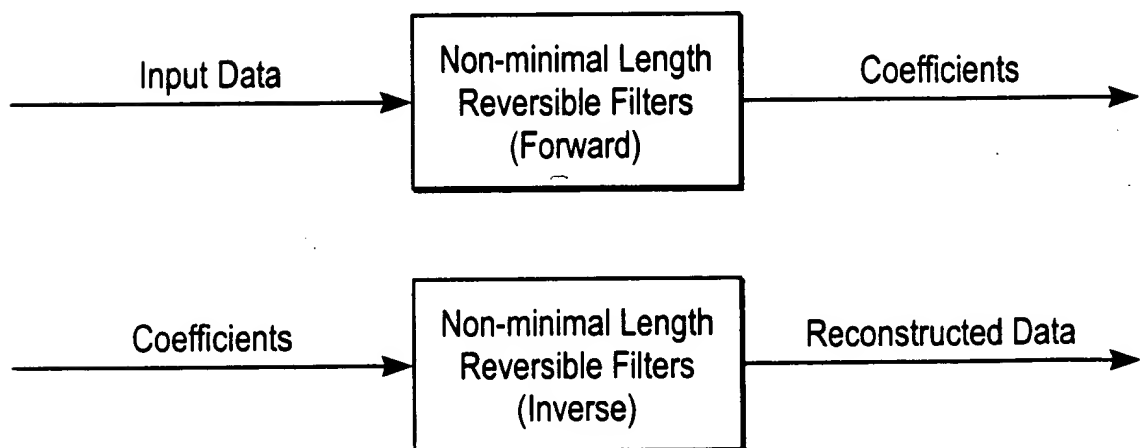


FIG. 3B

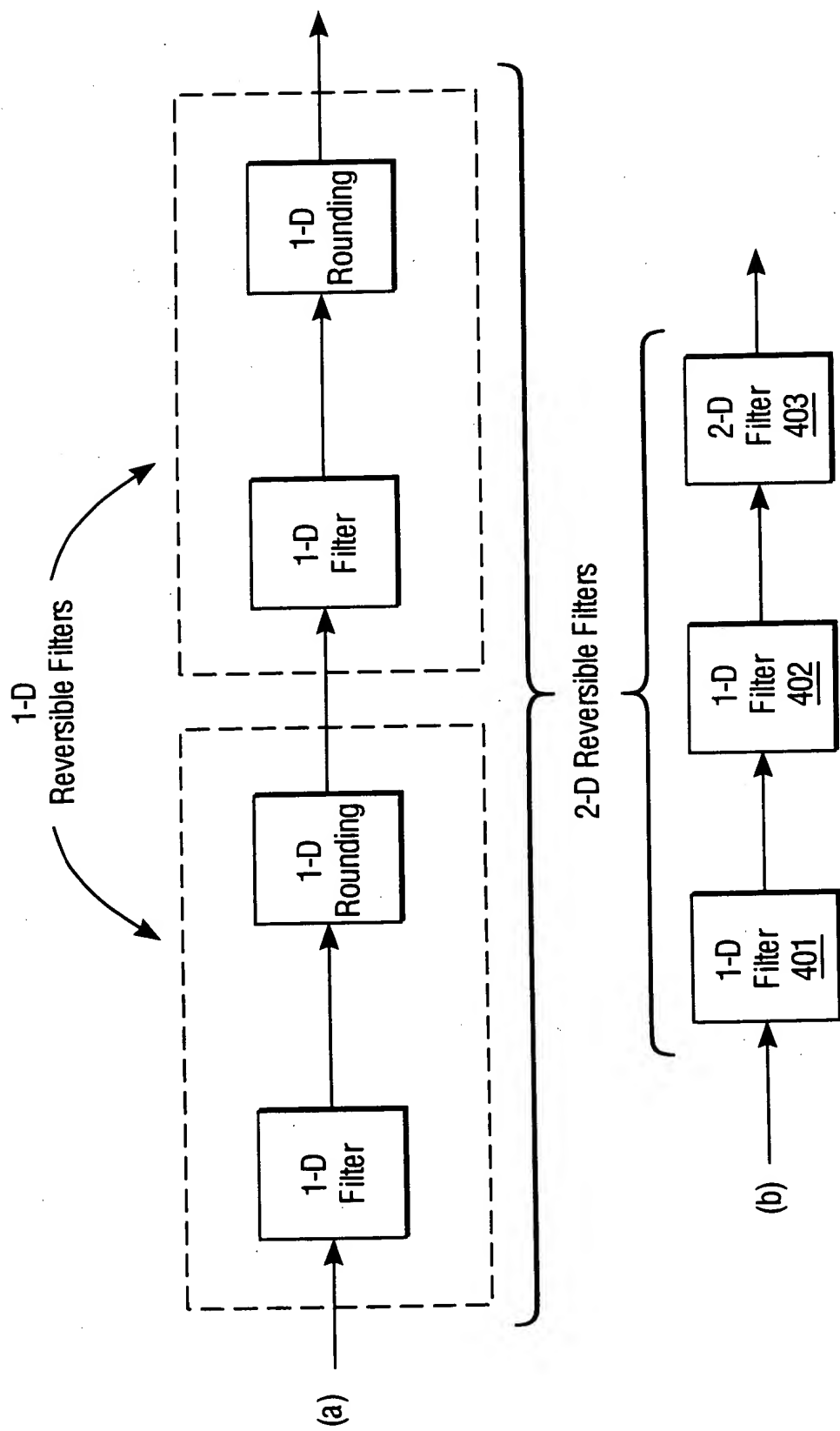


FIG. 4

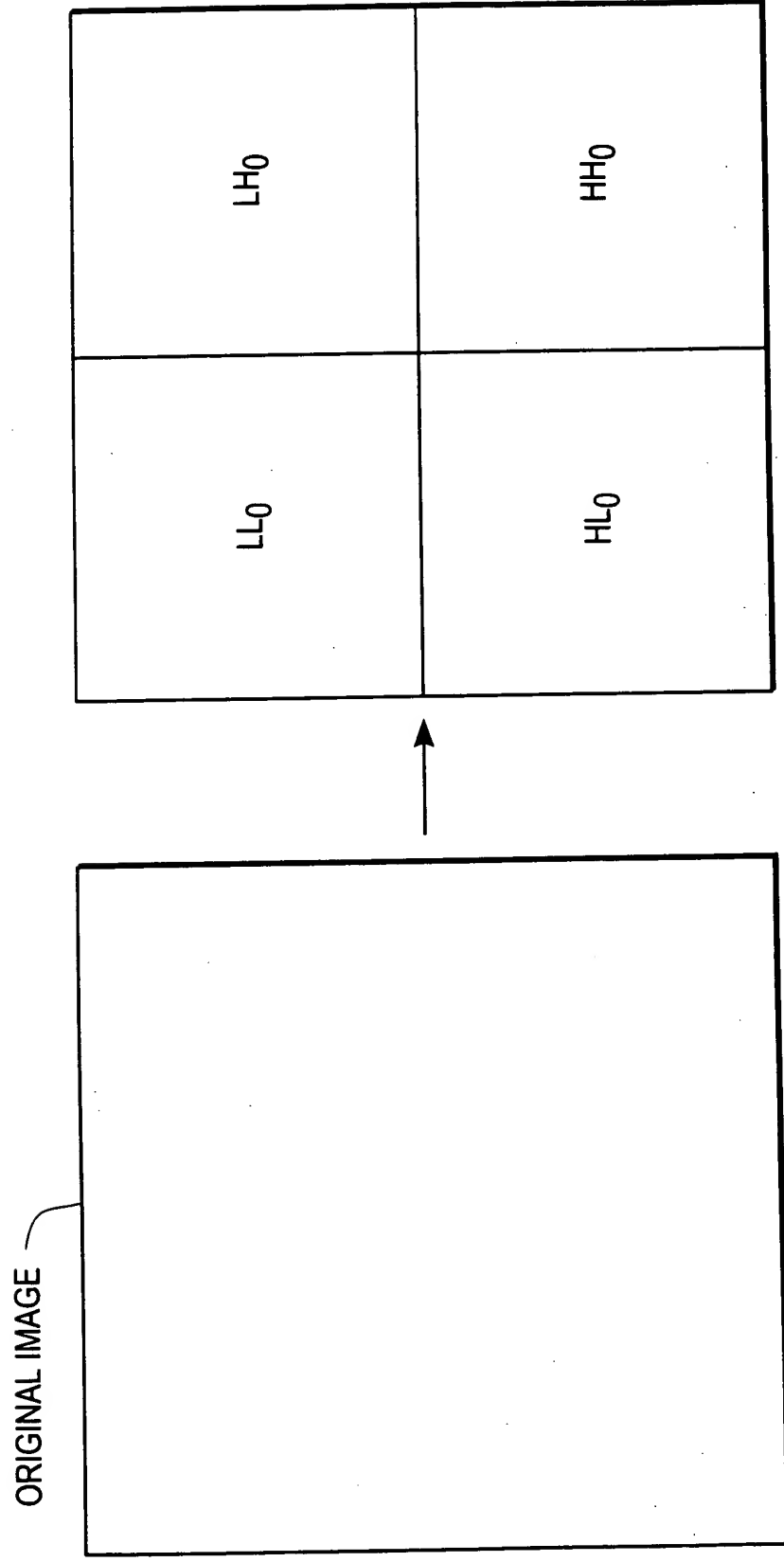


FIG. 5A

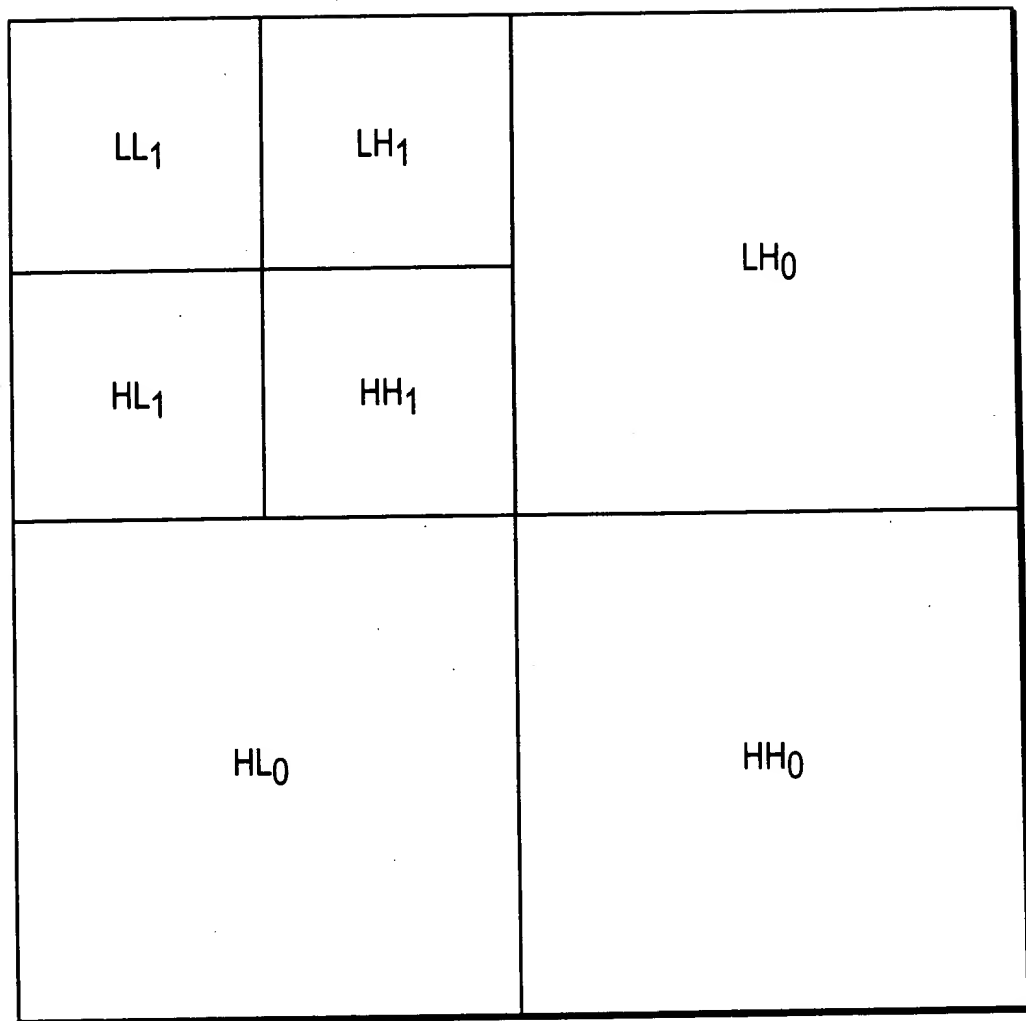


FIG. 5B

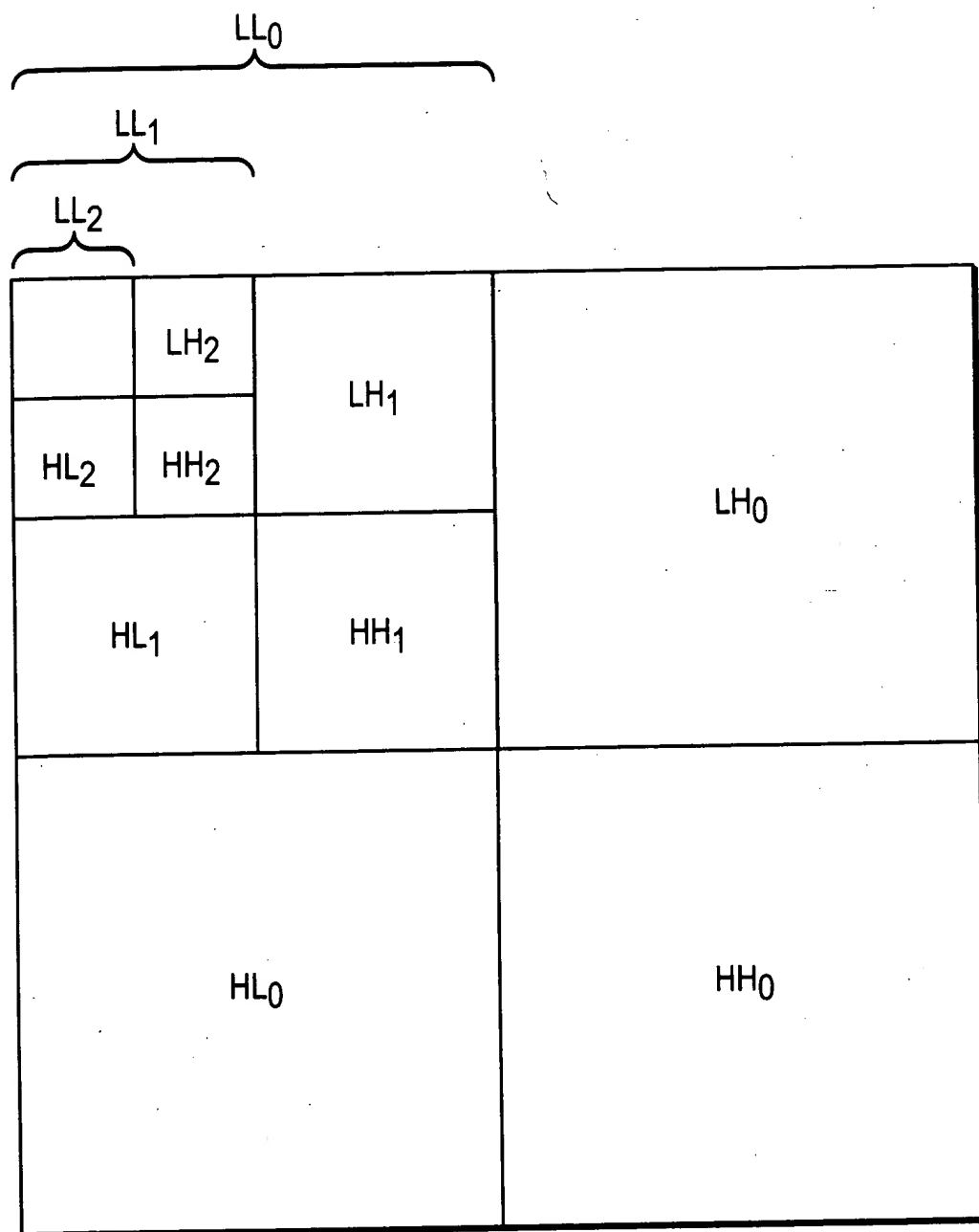


FIG. 5C

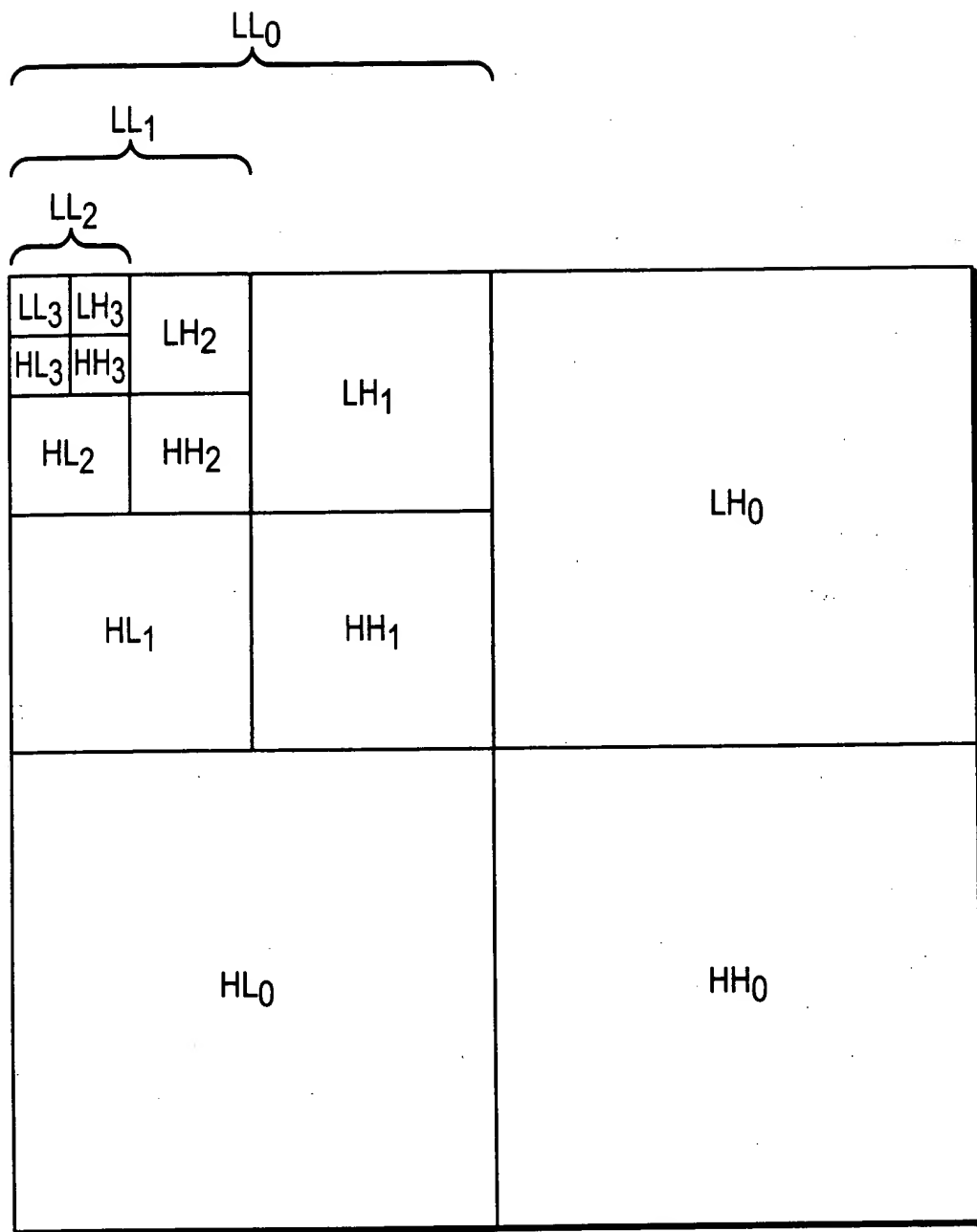


FIG. 5D

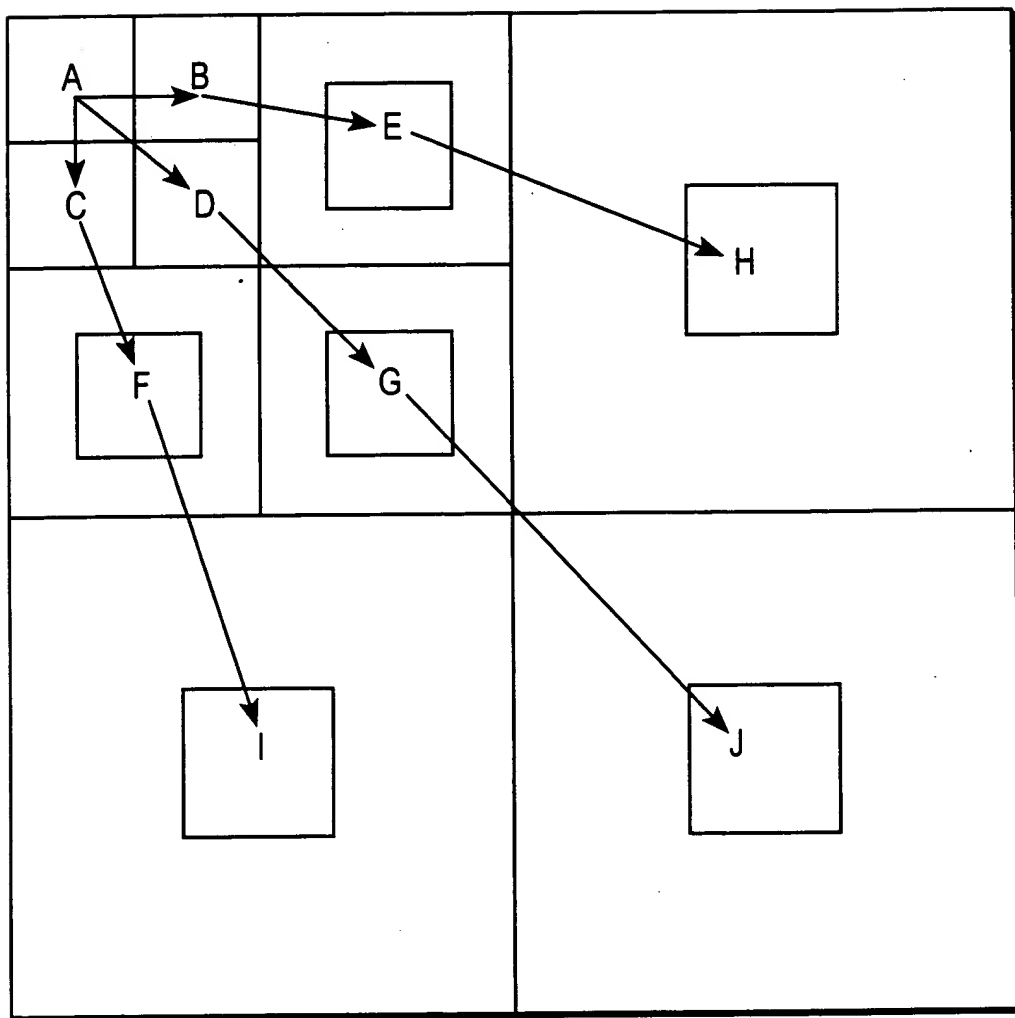


FIG. 6



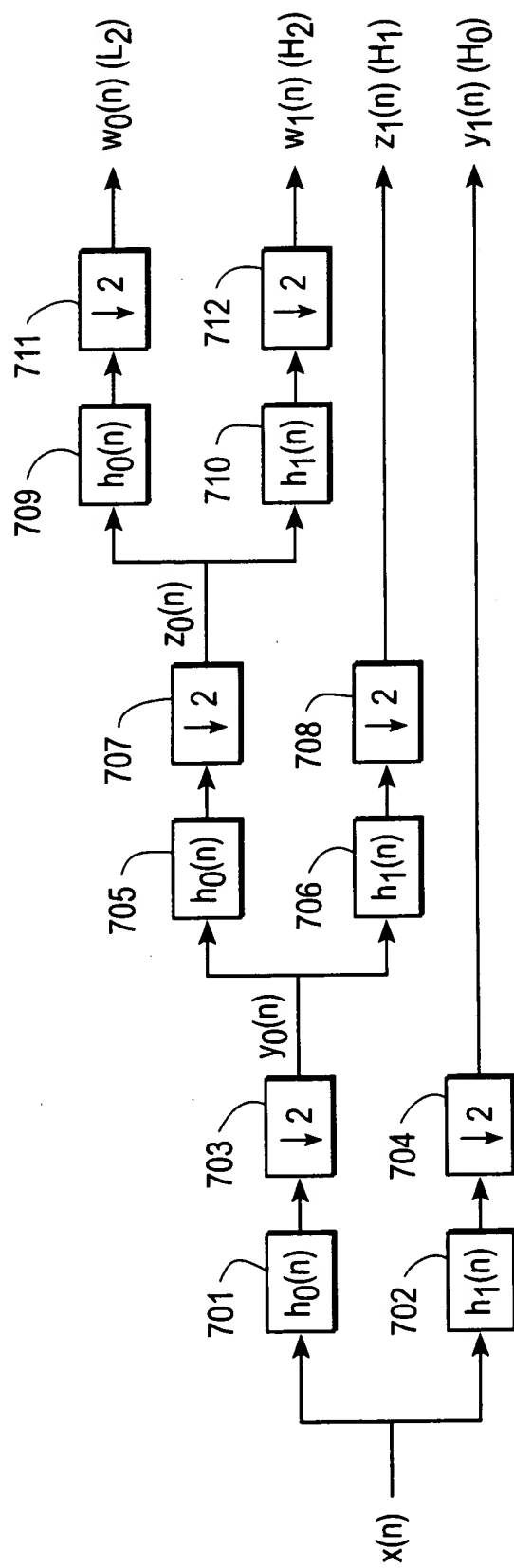


FIG. 7

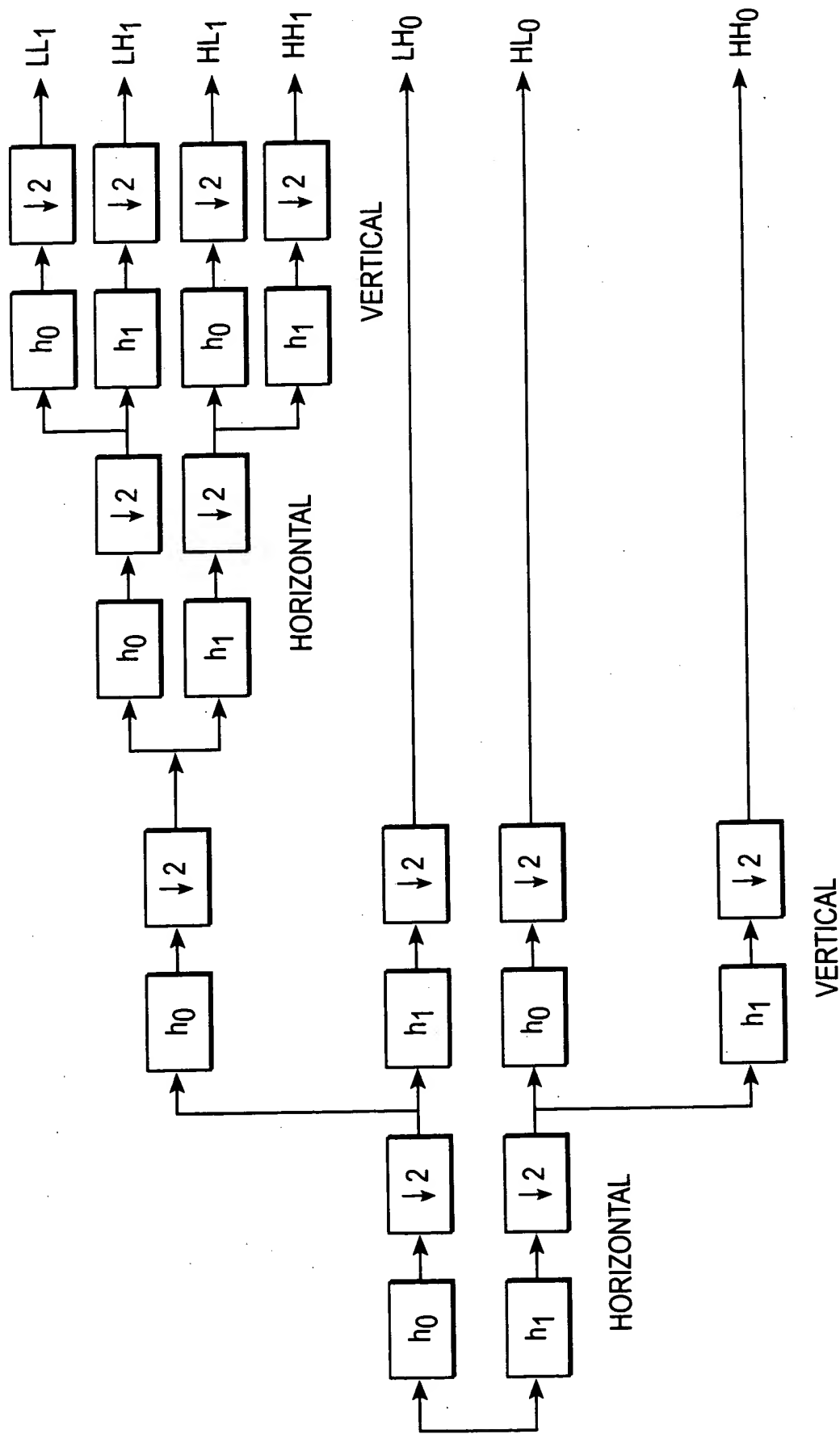


FIG. 8

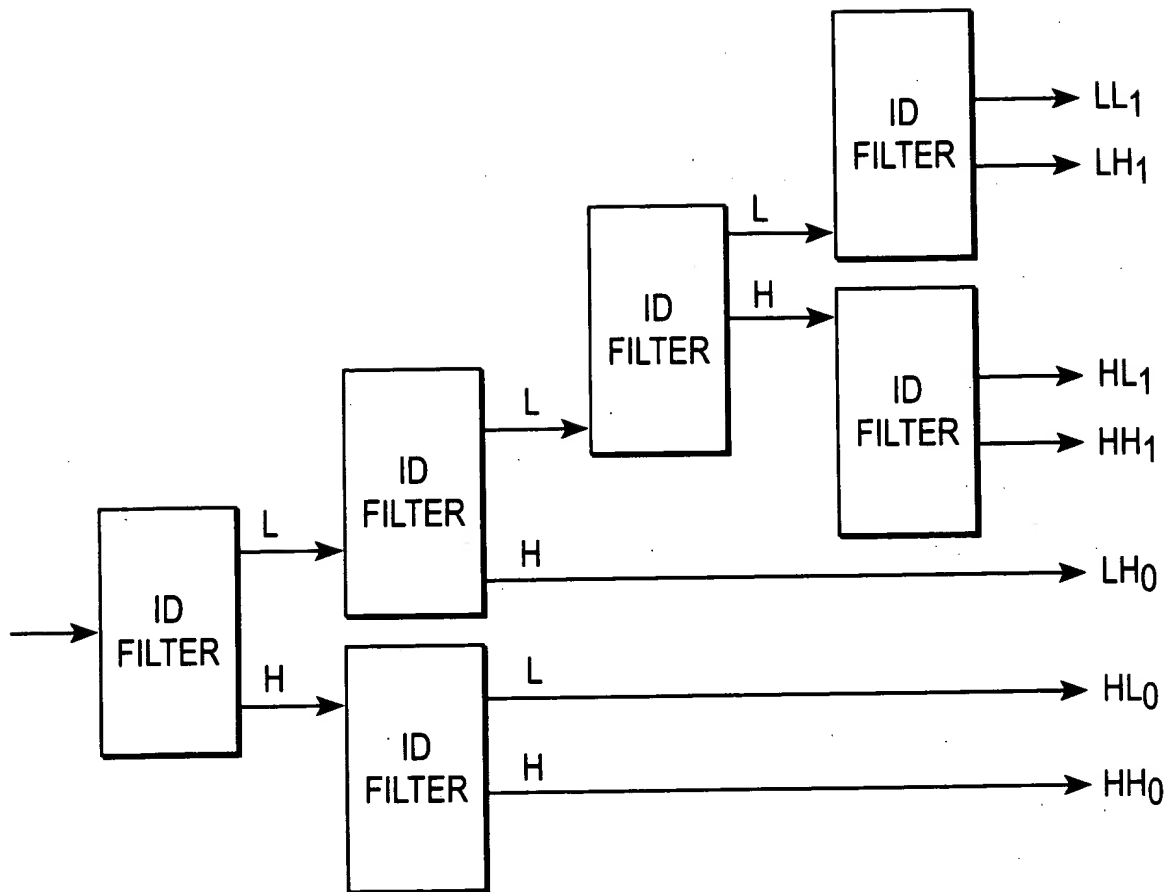


FIG. 9

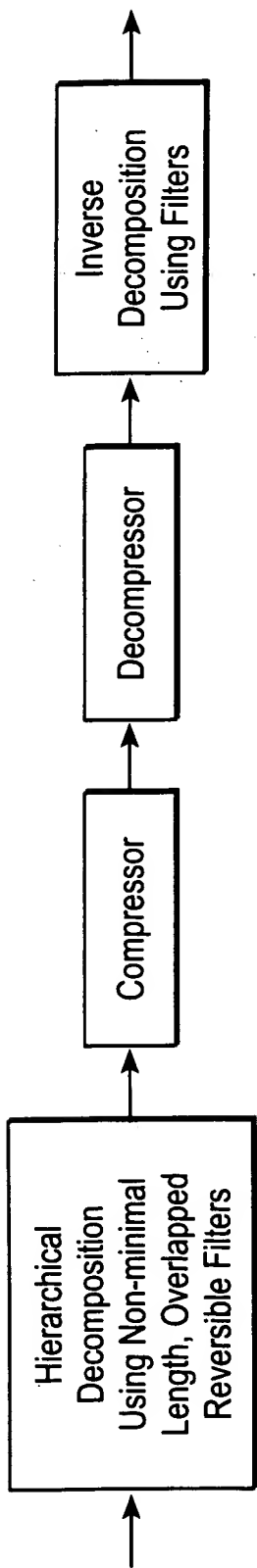


FIG. 10

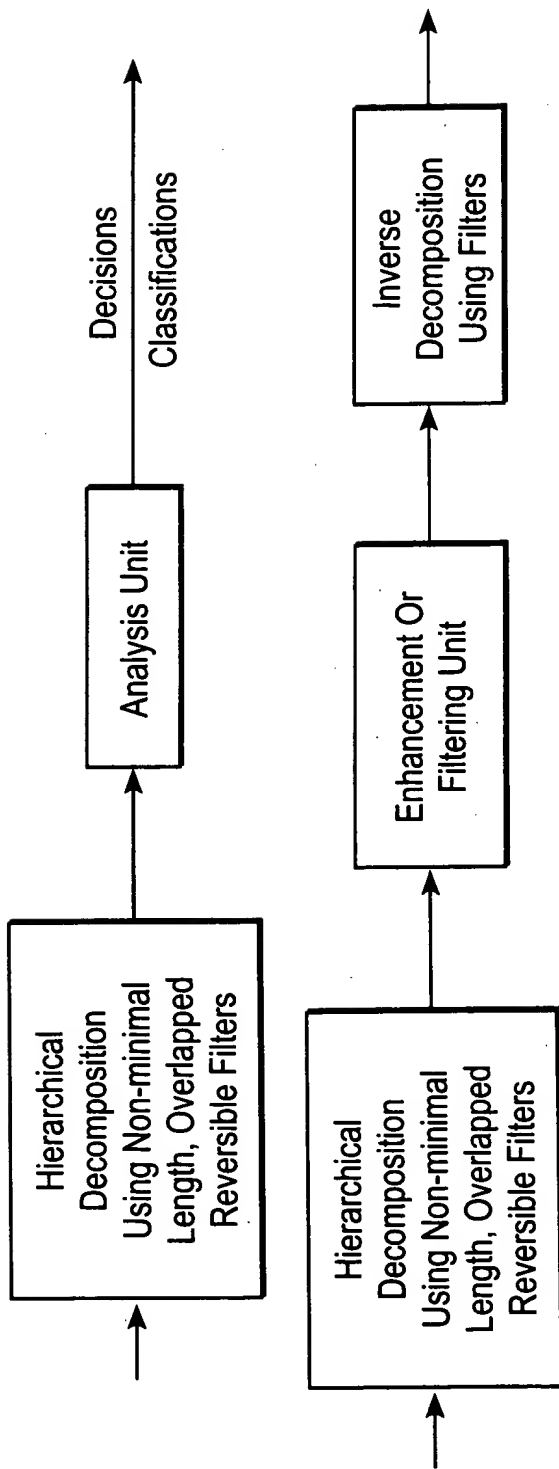


FIG. 11

|     |     |     |
|-----|-----|-----|
| b   | b+2 | b+2 |
| b+2 | b+4 |     |
| b+2 |     | b+4 |

FIG. 12A

|    |    |   |   |   |
|----|----|---|---|---|
|    | 16 | 8 | 4 | 2 |
| 16 | 8  |   |   |   |
| 8  |    | 4 |   |   |
| 4  |    |   | 2 |   |
| 2  |    |   |   |   |
| 1  |    |   |   |   |

FIG. 12B

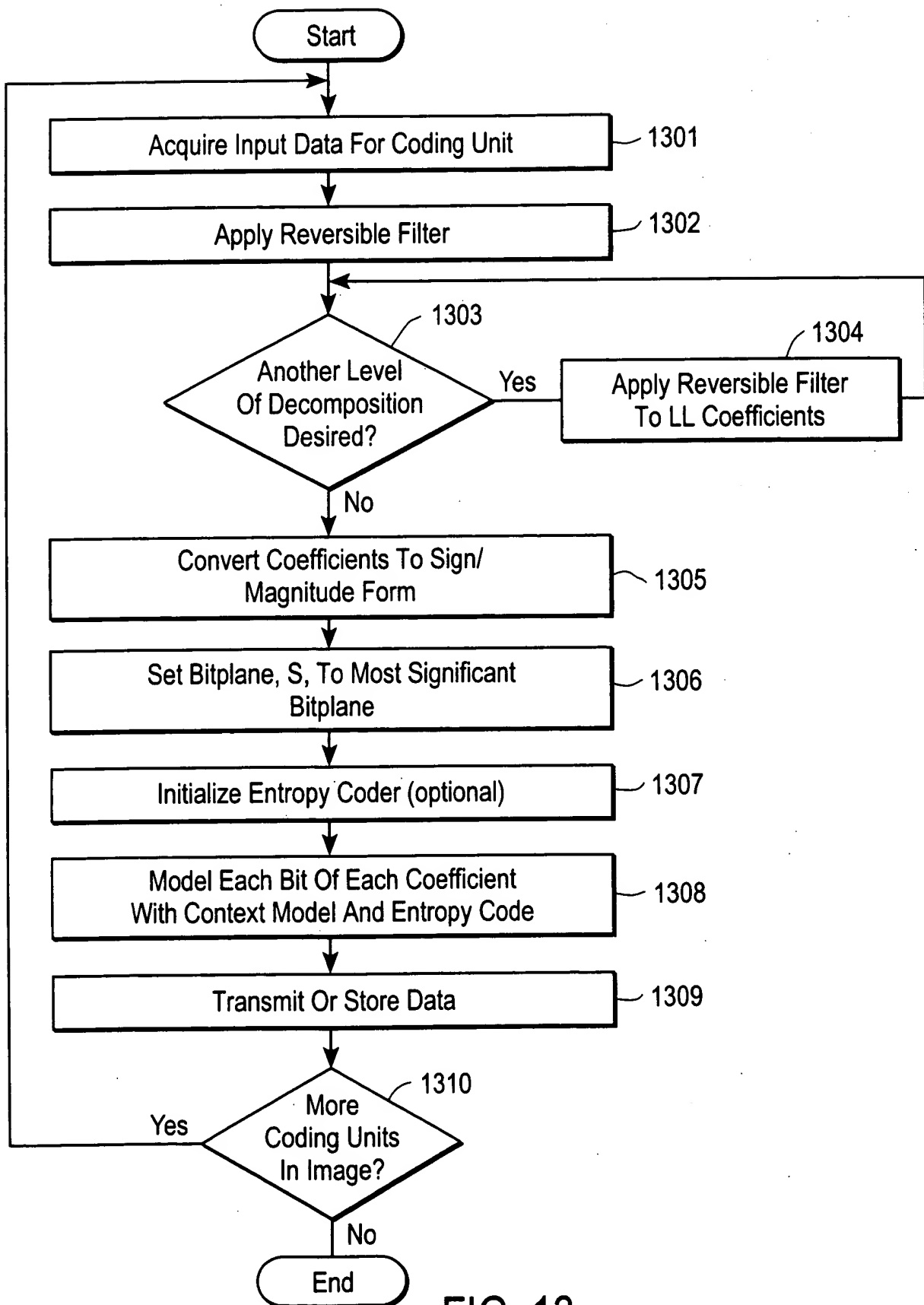


FIG. 13

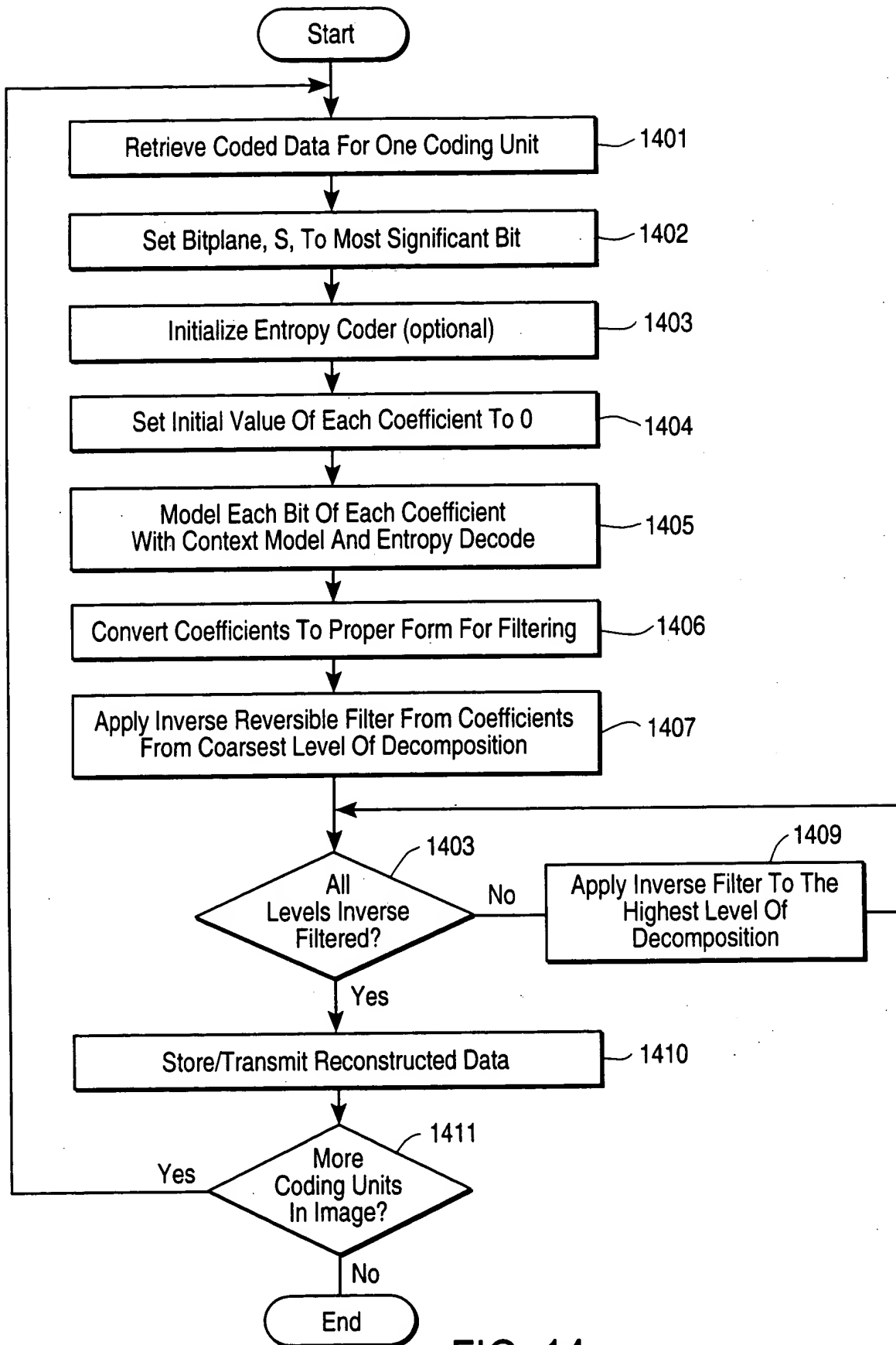


FIG. 14

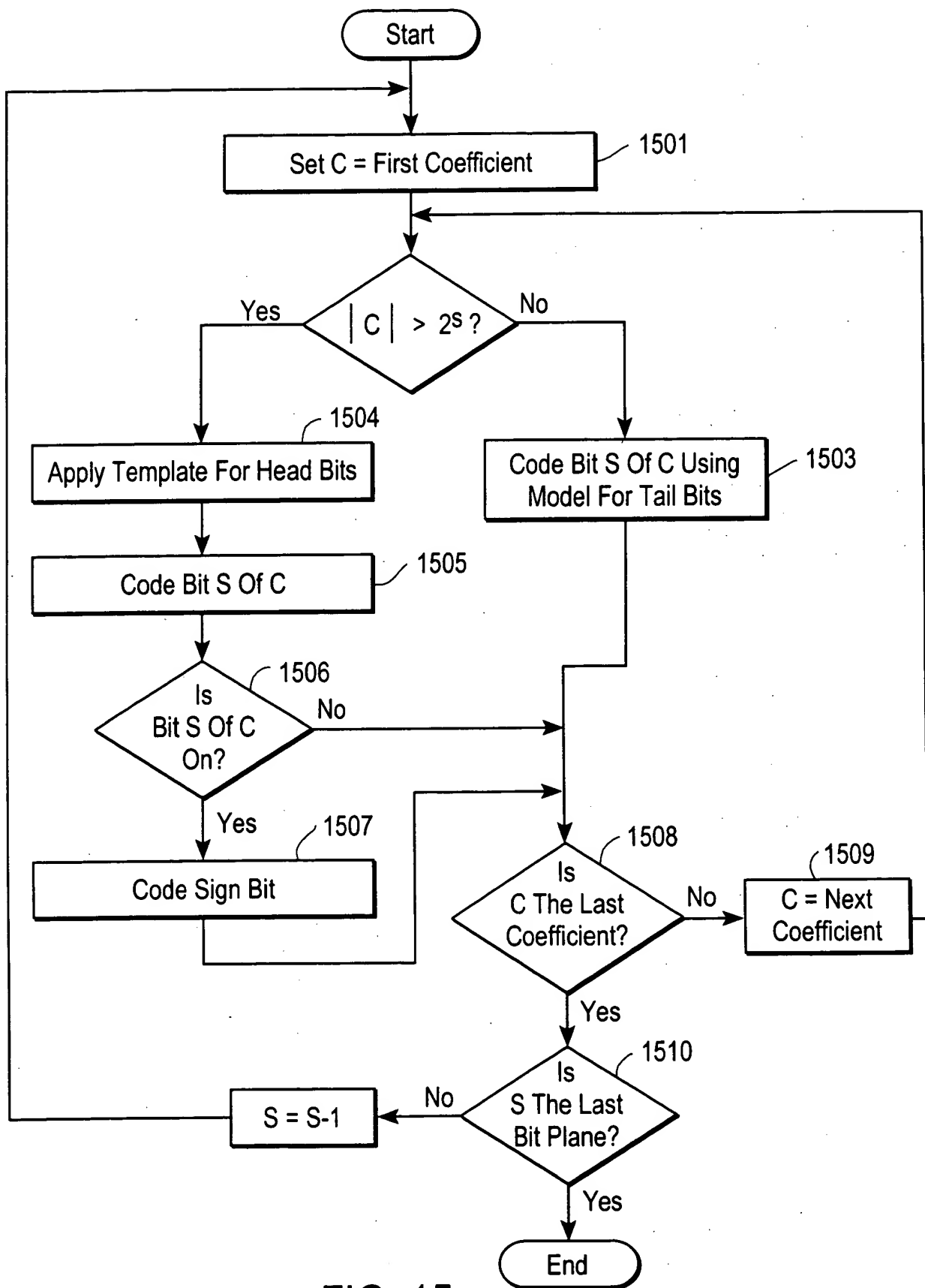


FIG. 15



1600

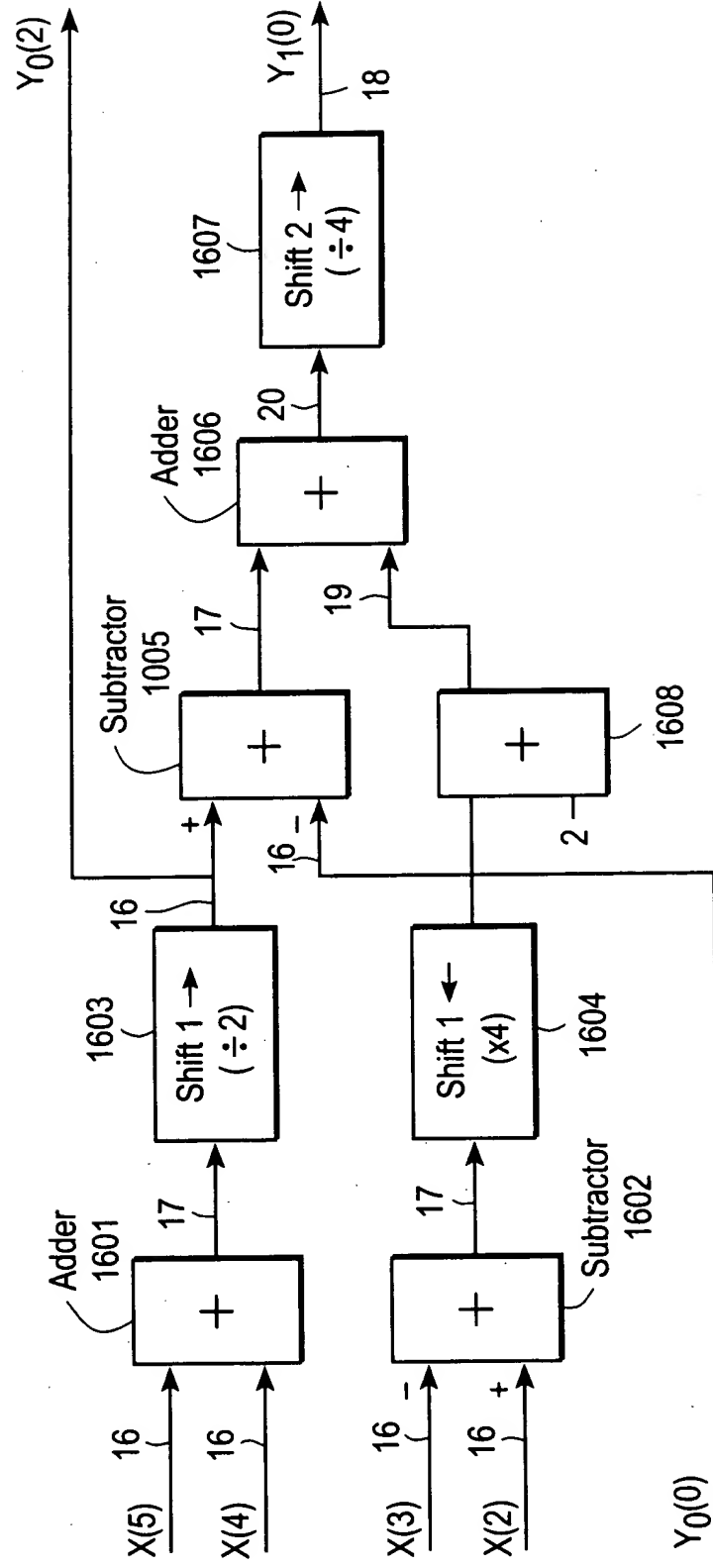


FIG. 16

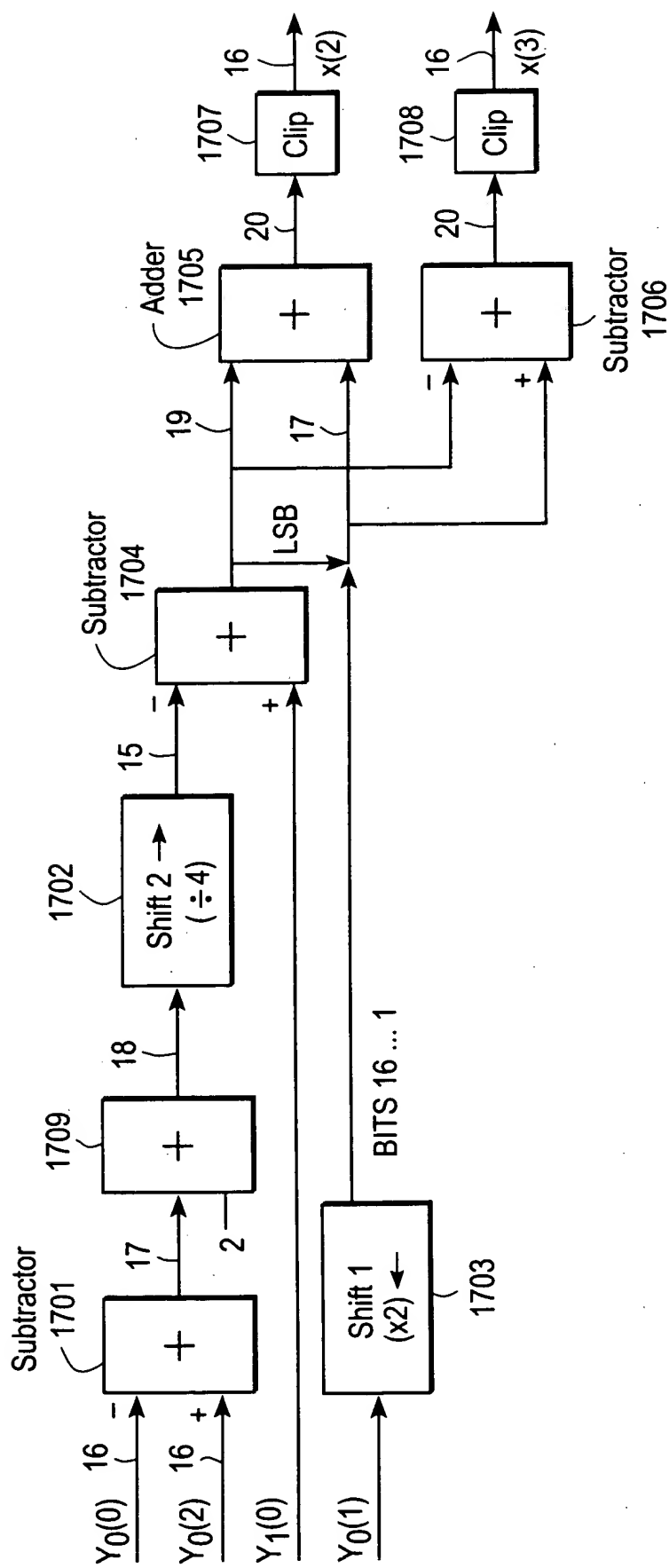


FIG. 17

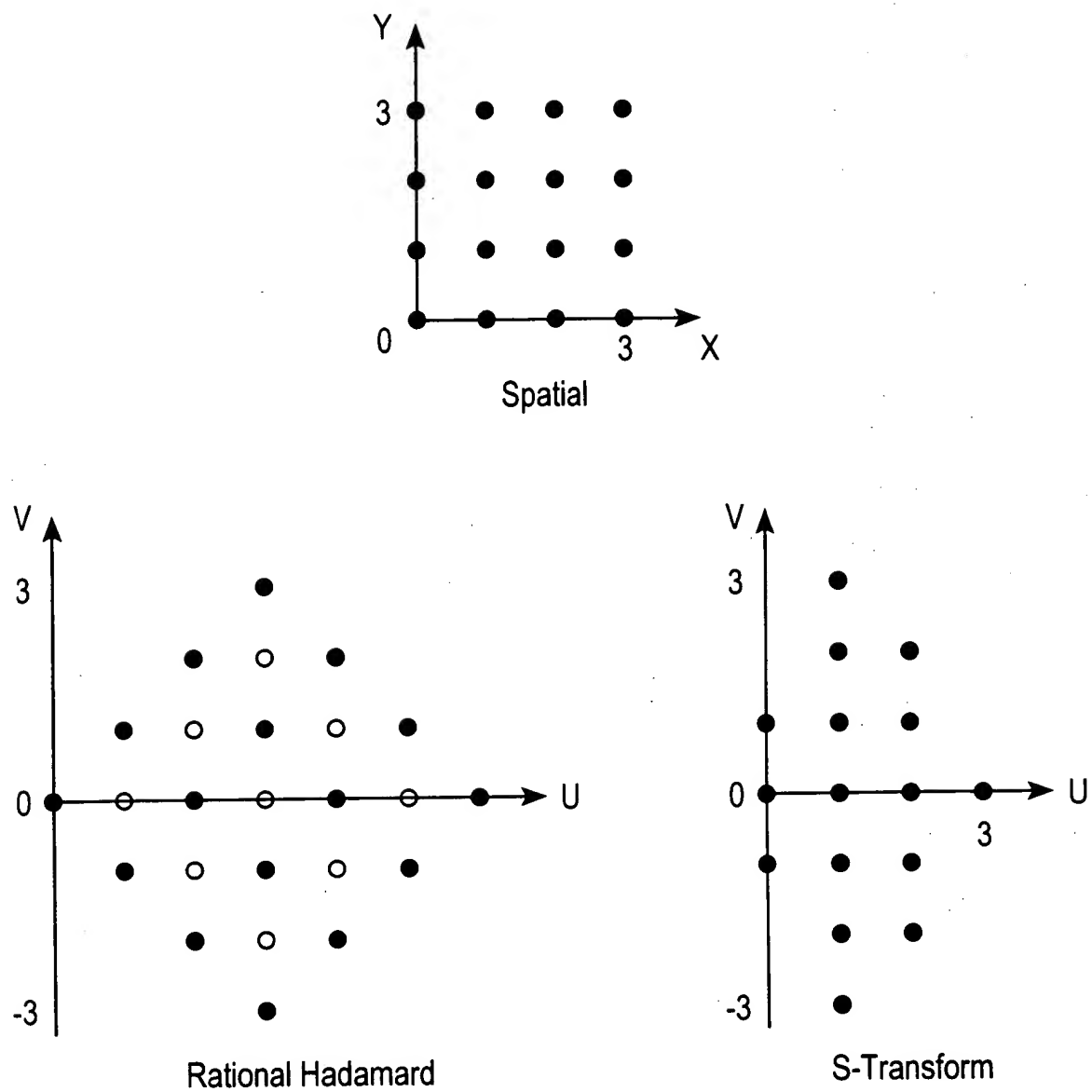


FIG. 18

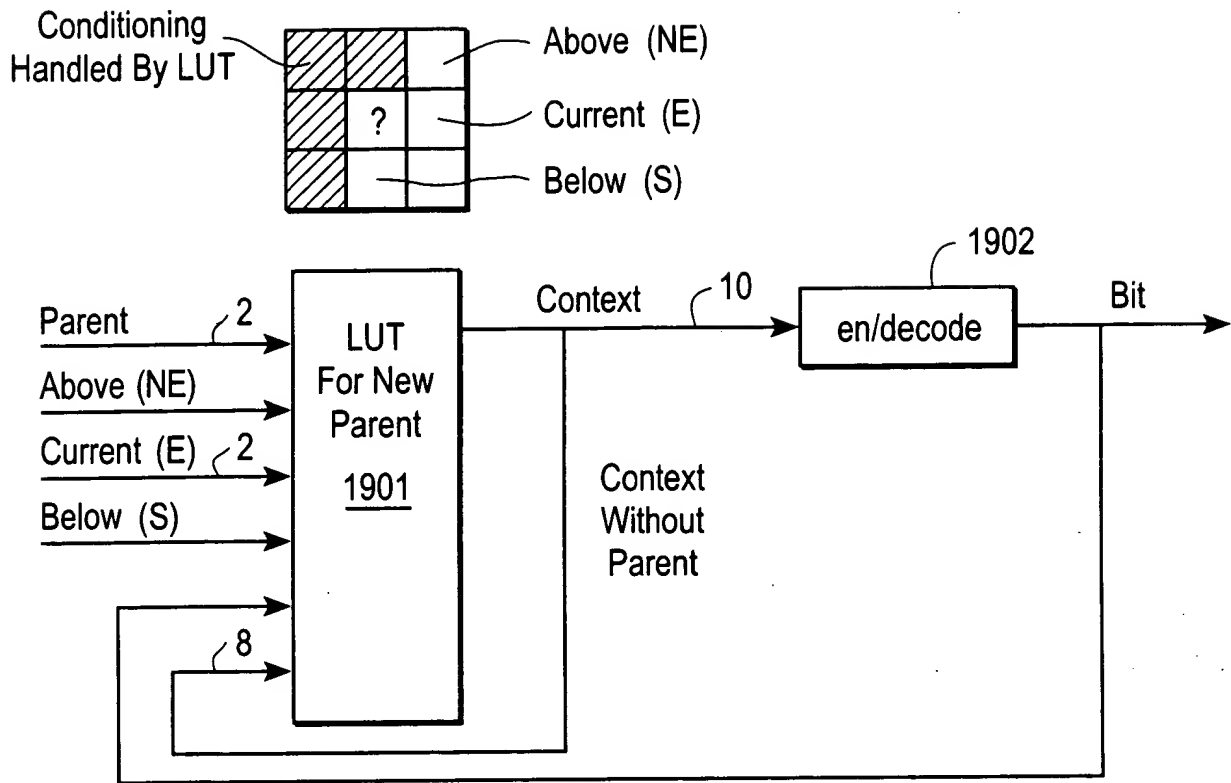


FIG. 19A

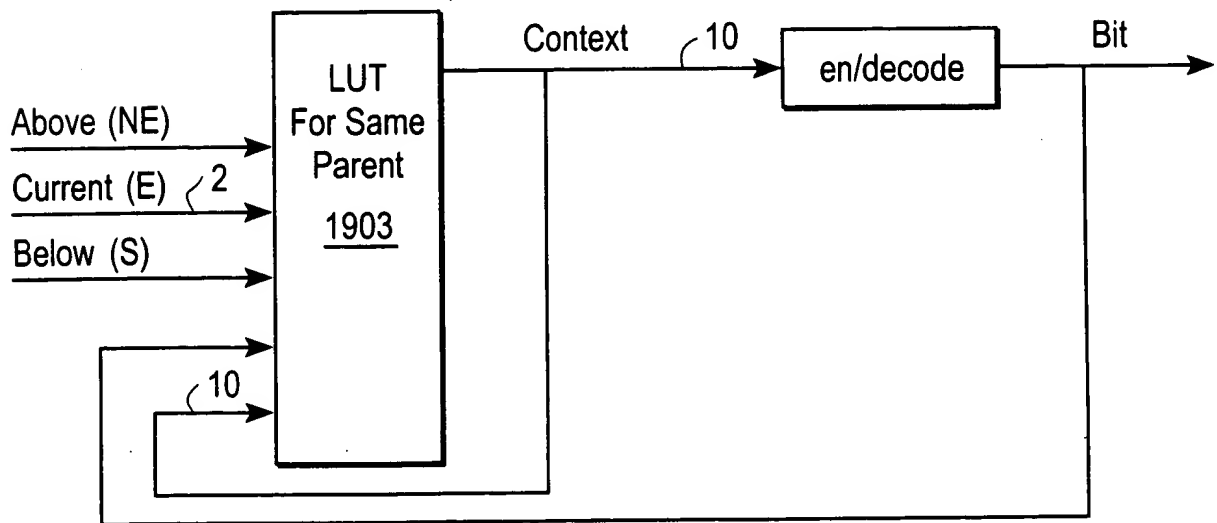


FIG. 19B

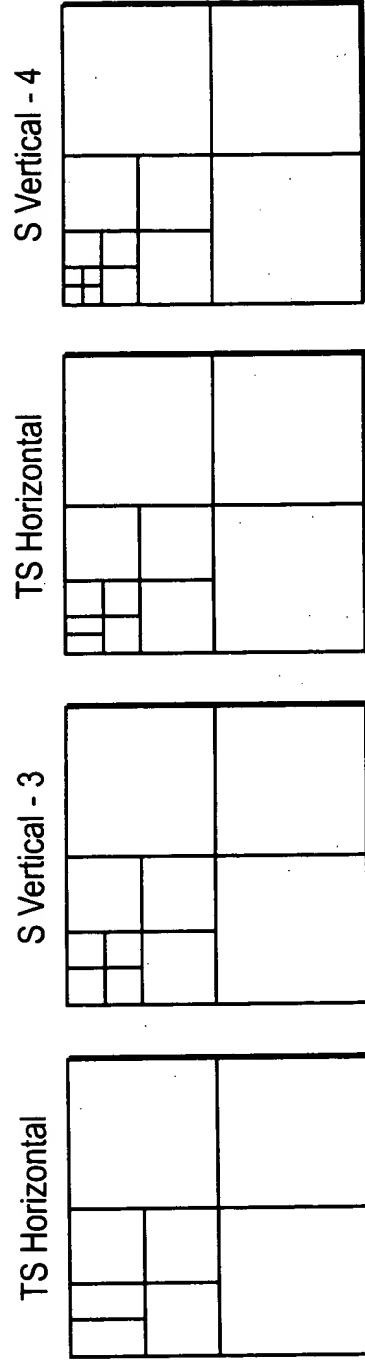
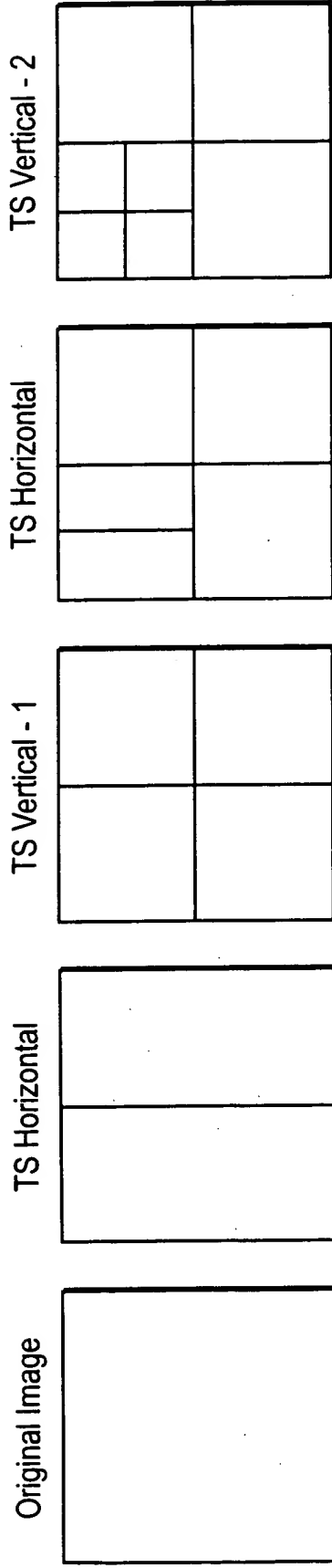


FIG. 20

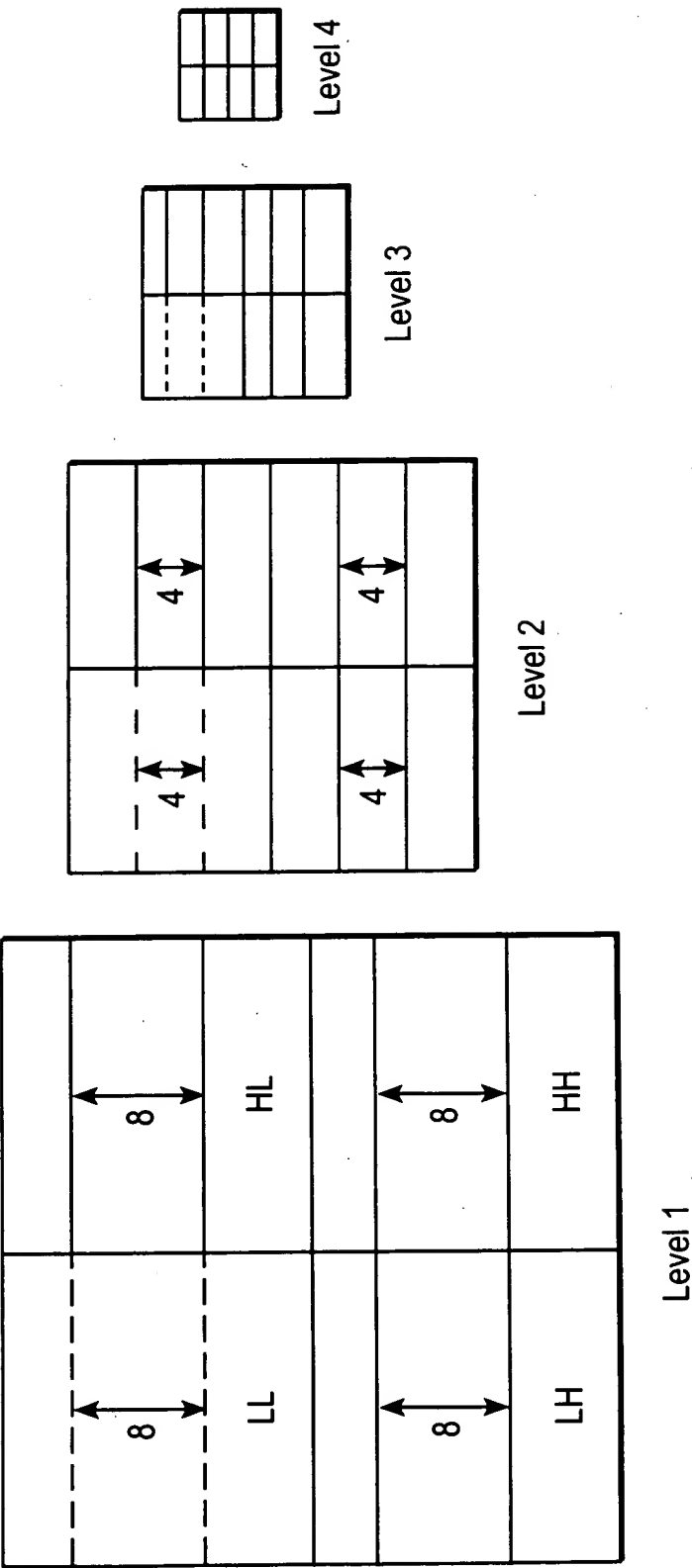


FIG. 21

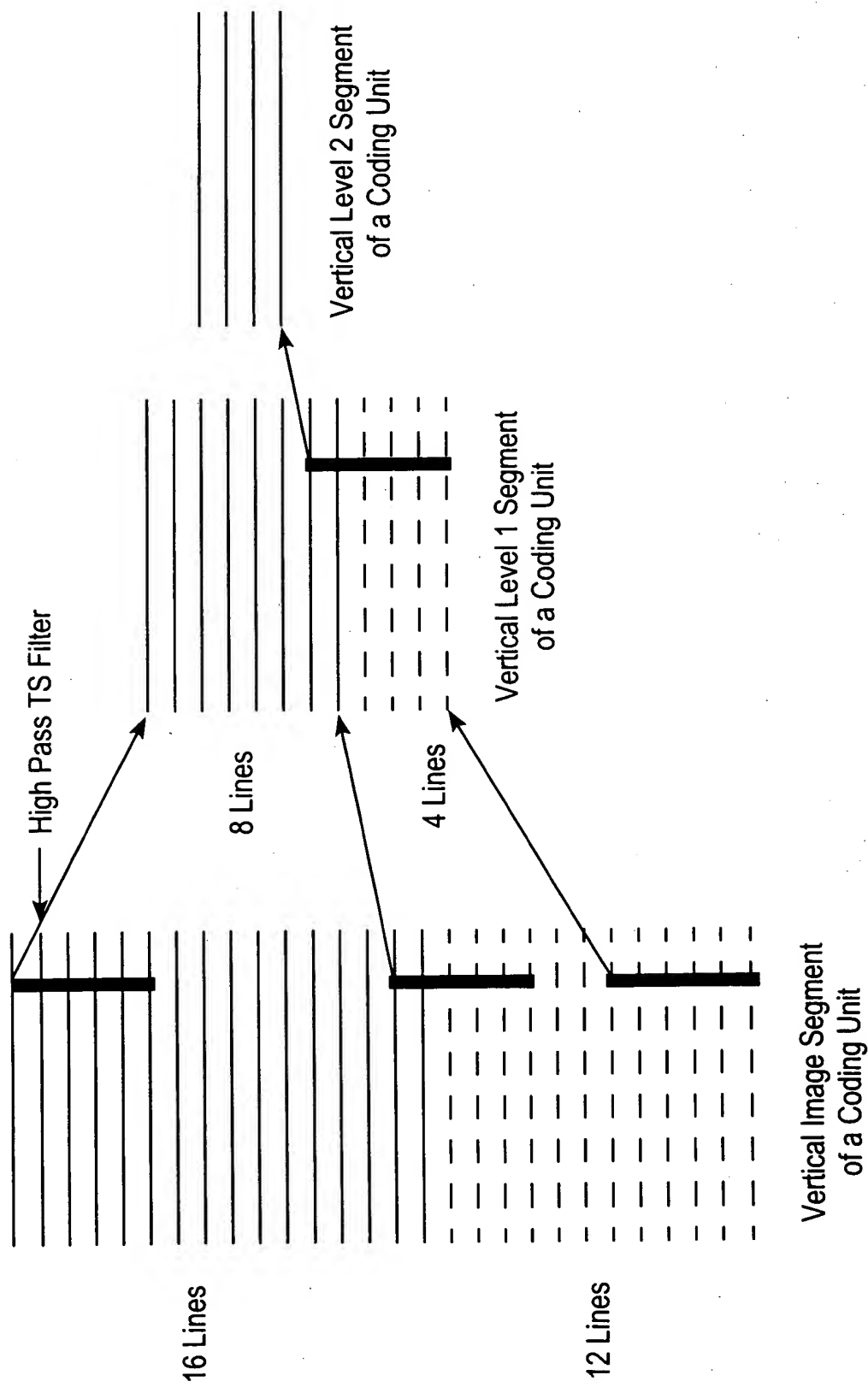


FIG. 22

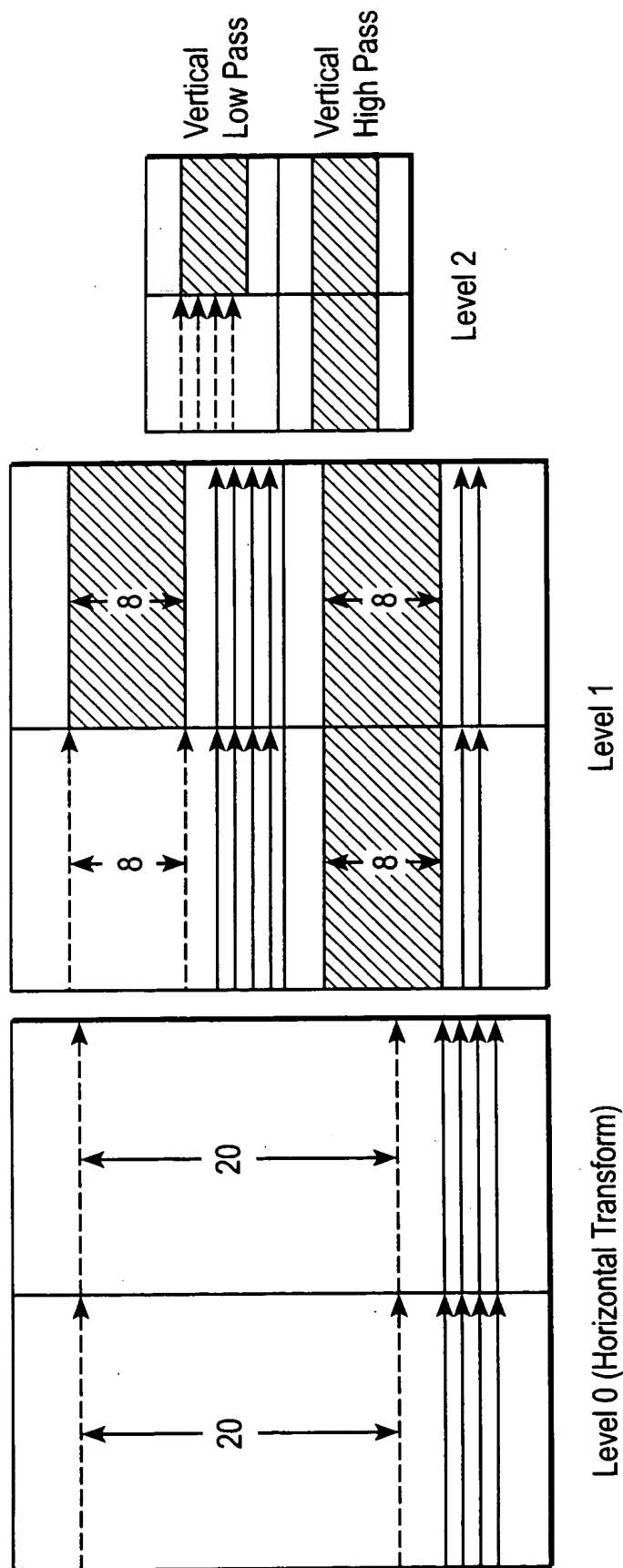


FIG. 23



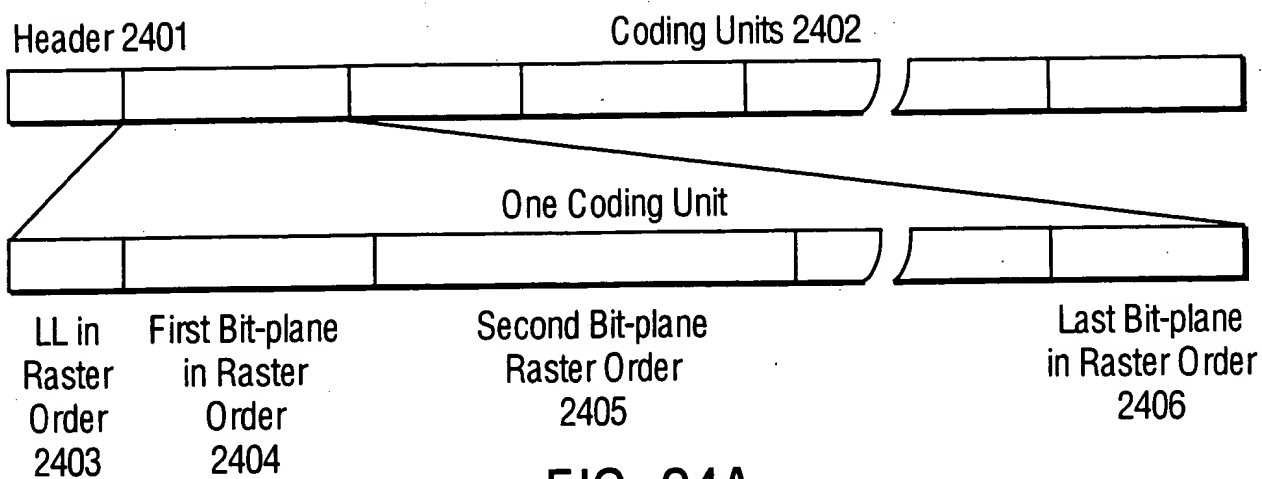


FIG. 24A

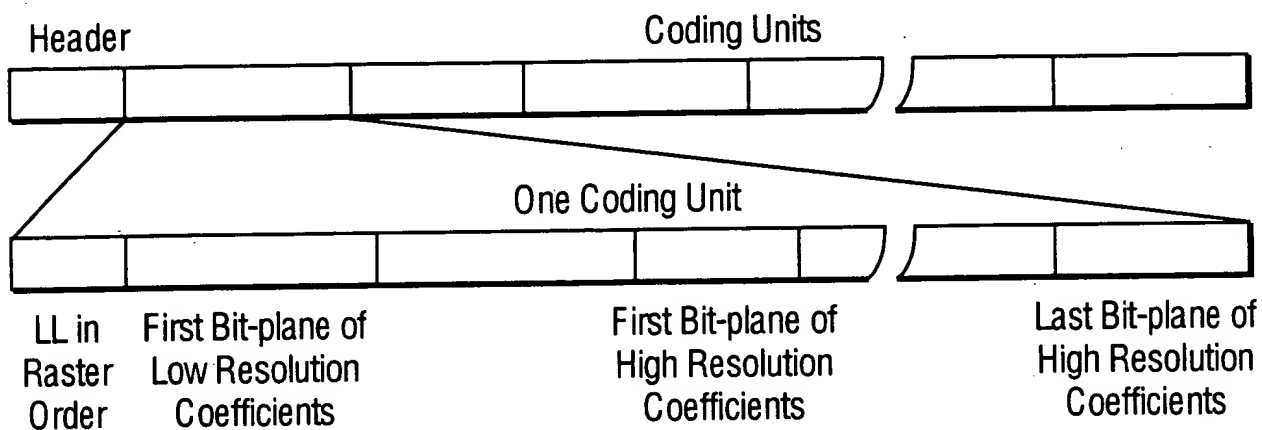


FIG. 24B

|    |   |    |
|----|---|----|
| NW | N | NE |
| W  | P | E  |
| SW | S | SE |

FIG. 25

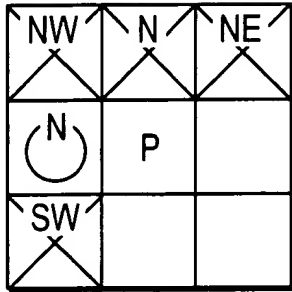
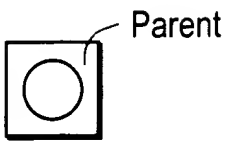


FIG. 26A

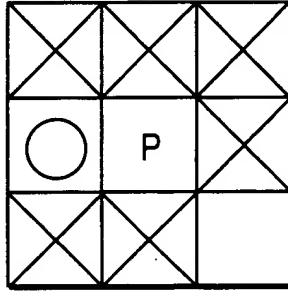
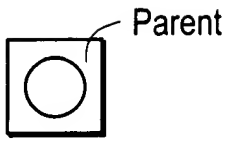


FIG. 26B

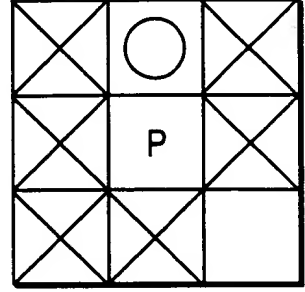
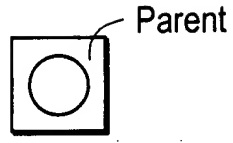
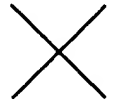


FIG. 26C



= Tail-On Bit (1 Bit)



= Tail-On Information (2 Bits)

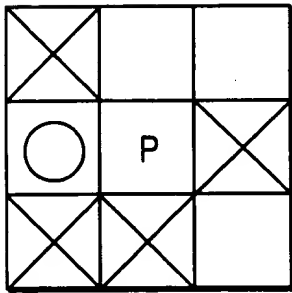


FIG. 26D

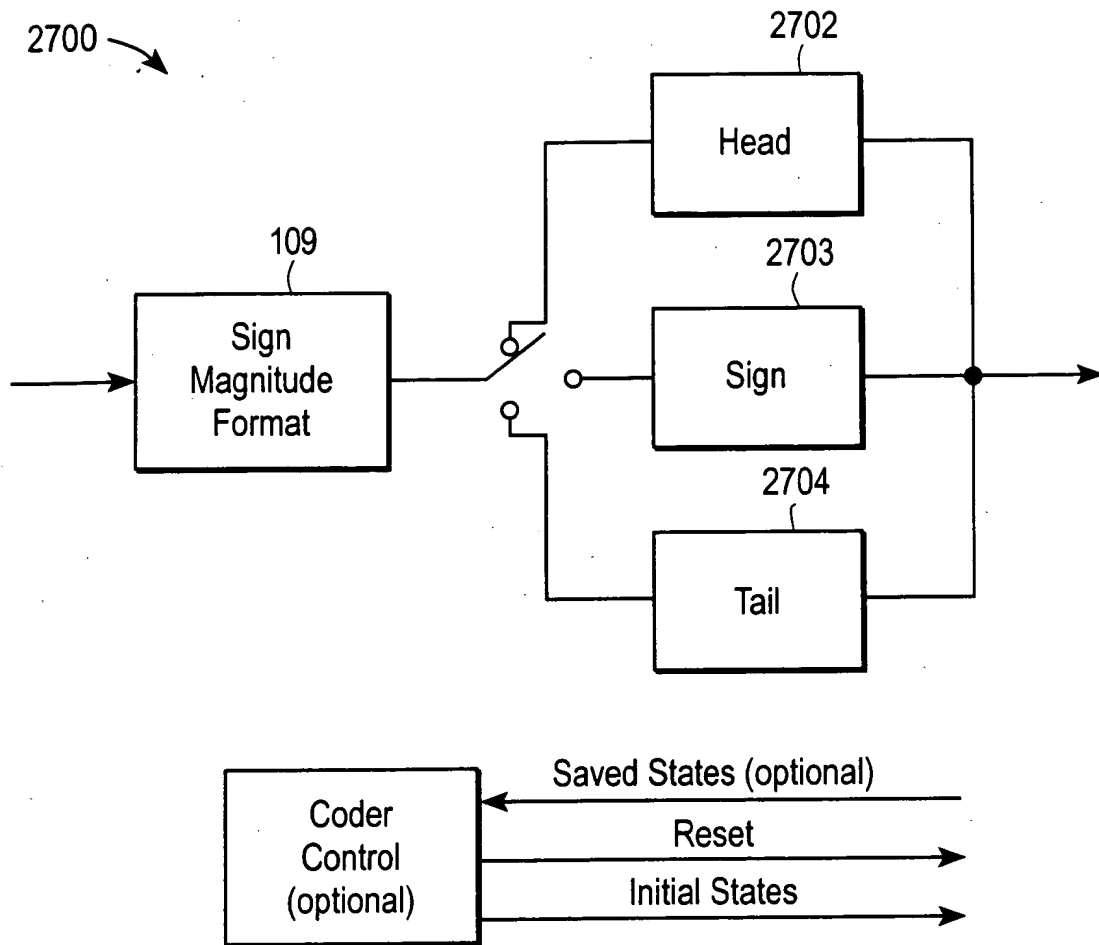


FIG. 27

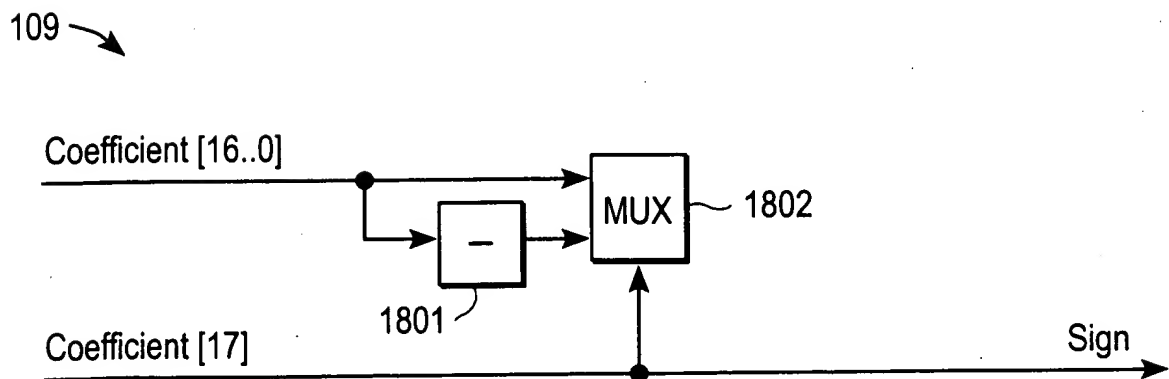


FIG. 28

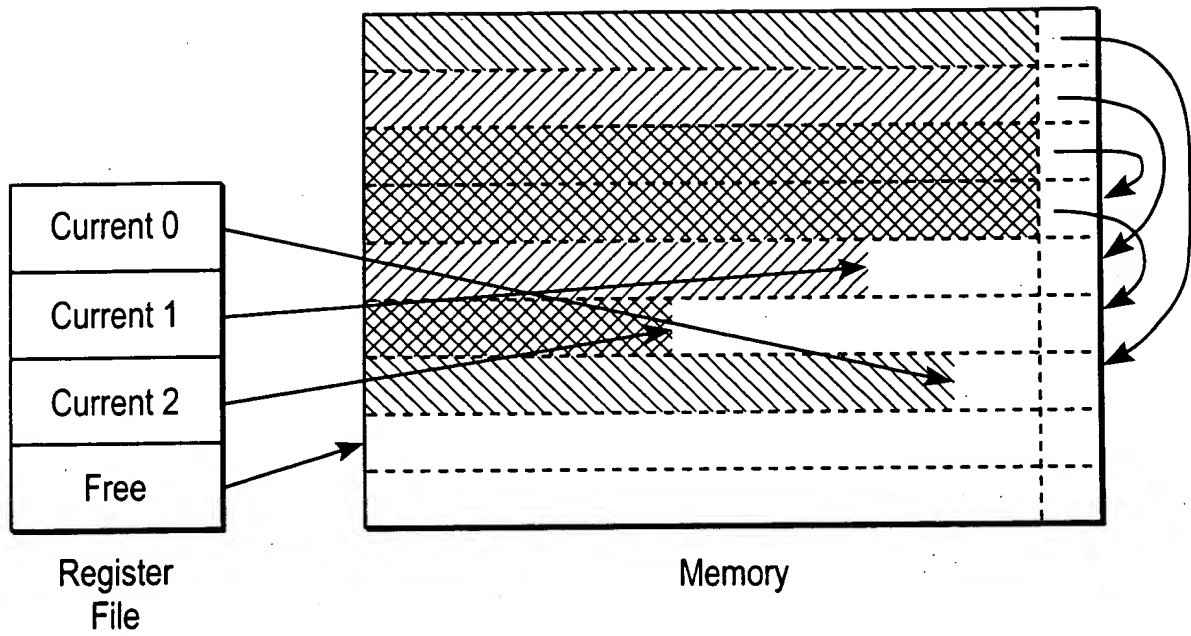


FIG. 29

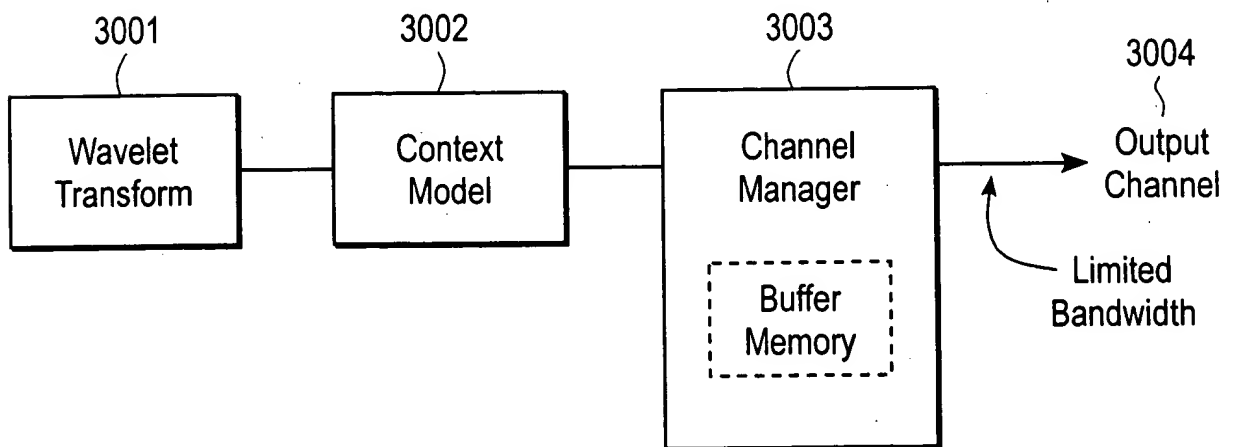


FIG. 30

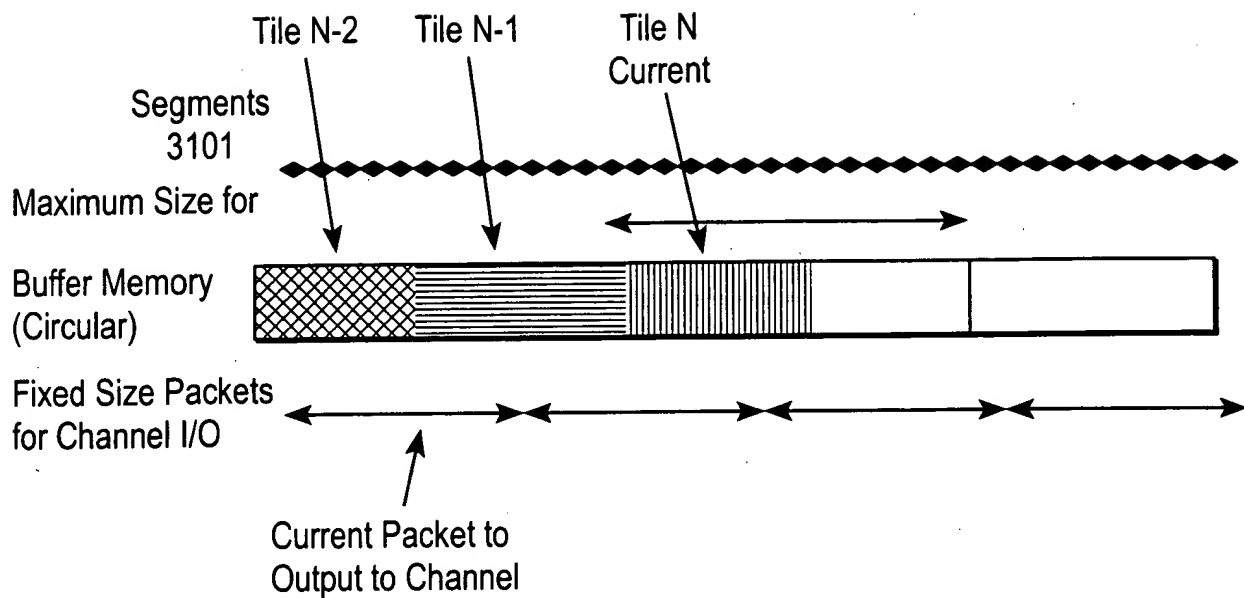


FIG. 31

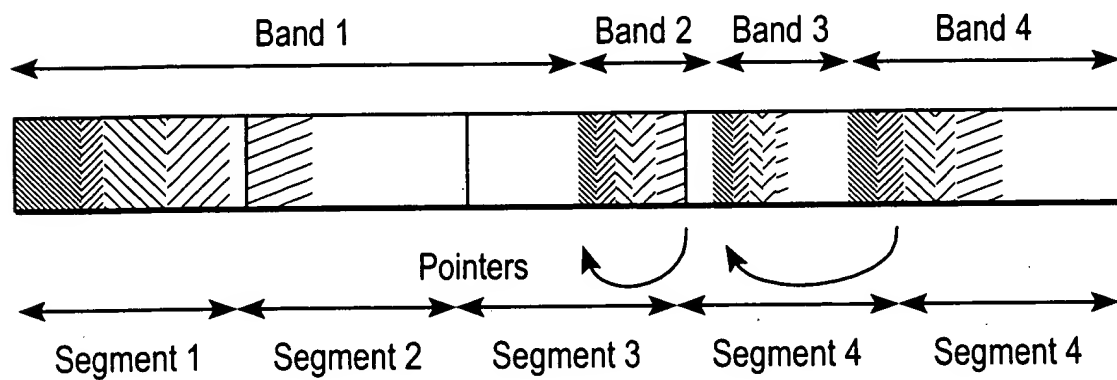


FIG. 32

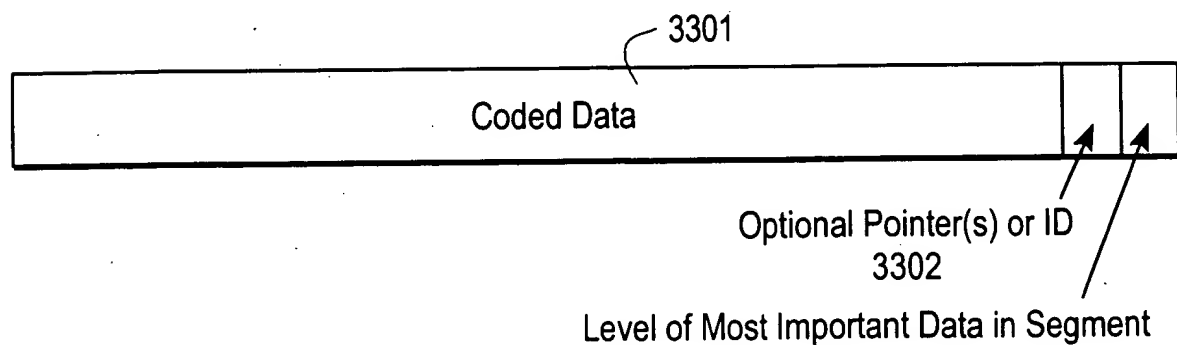


FIG. 33

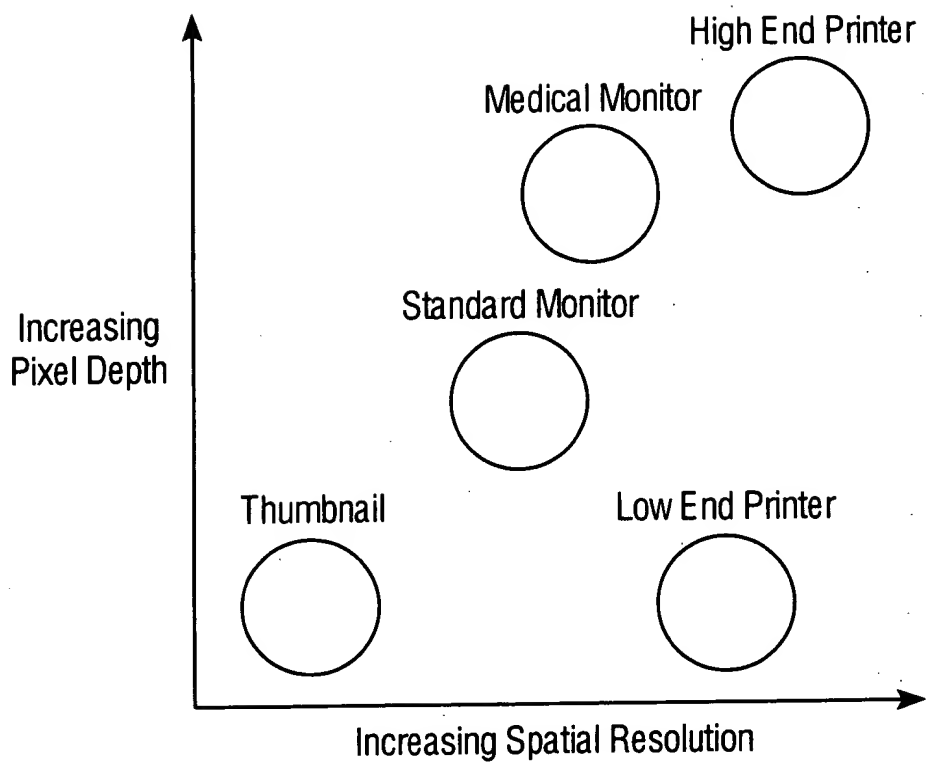


FIG. 34

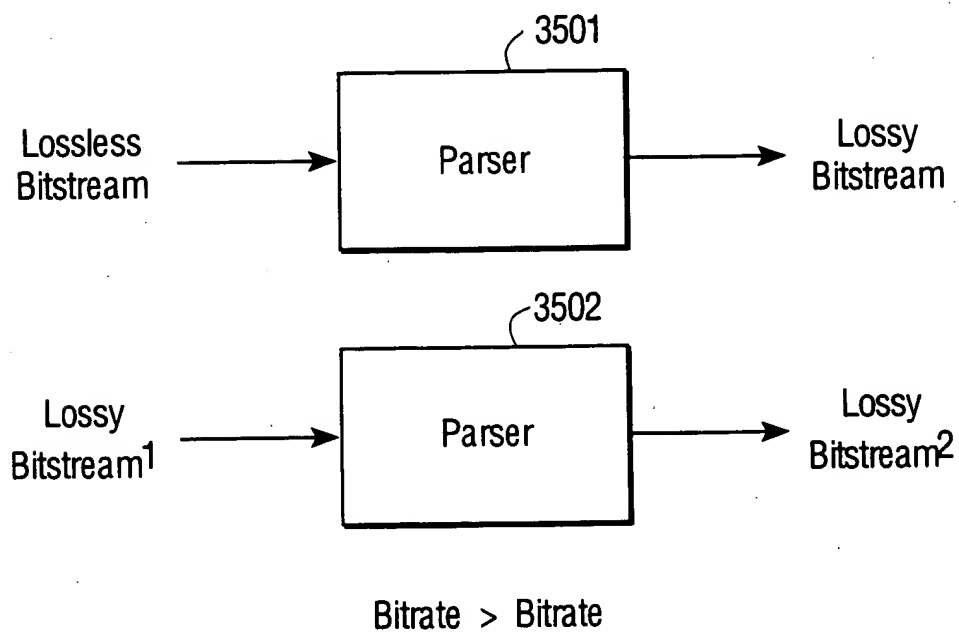


FIG. 35